

FIG. 4

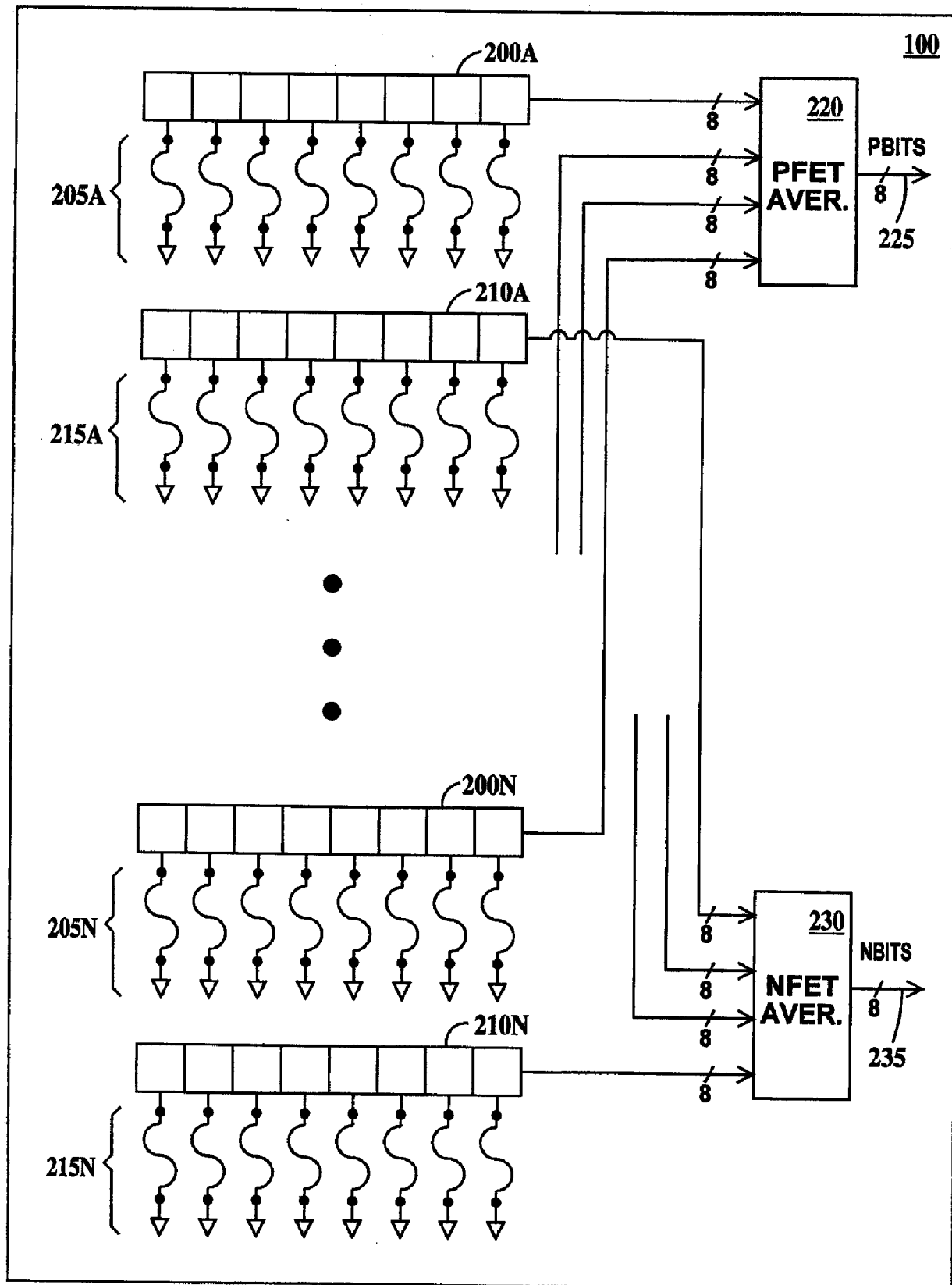


FIG. 5

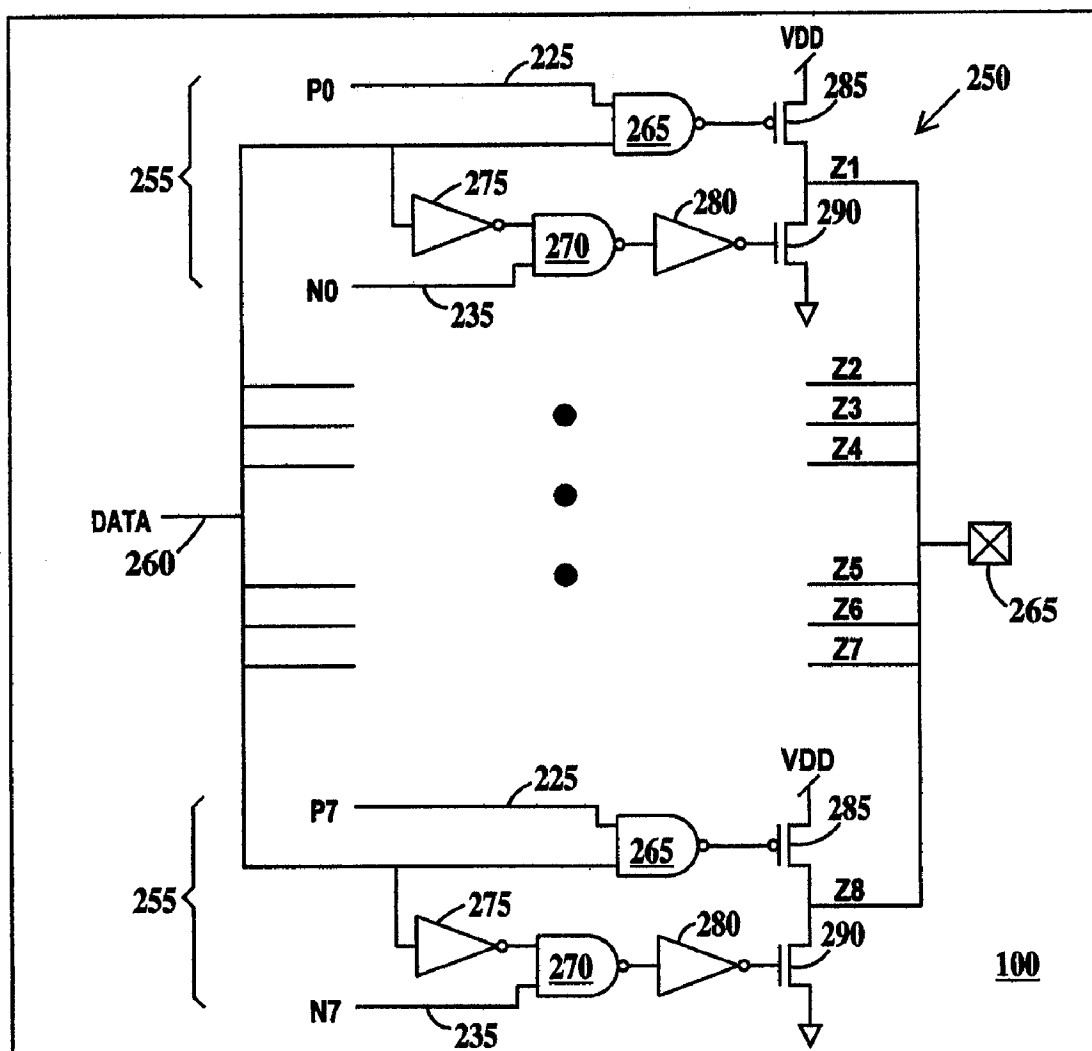


FIG. 6A

DRIVER IMPEDANCE (TARGET = 40 OHMS)				
	FUSE REGISTER VALUE	IMPEDANCE (ohms)		
		25C	55C	85C
NFET COUNT (NOMINAL PROCESS)	10000000	39.1	40.2	42.2
NFET COUNT (WC PROCESS)	11111111	38.3	40.7	41.4
NFET COUNT (BC PROCESS)	00000000	38.8	40.6	41.2

FIG. 6B

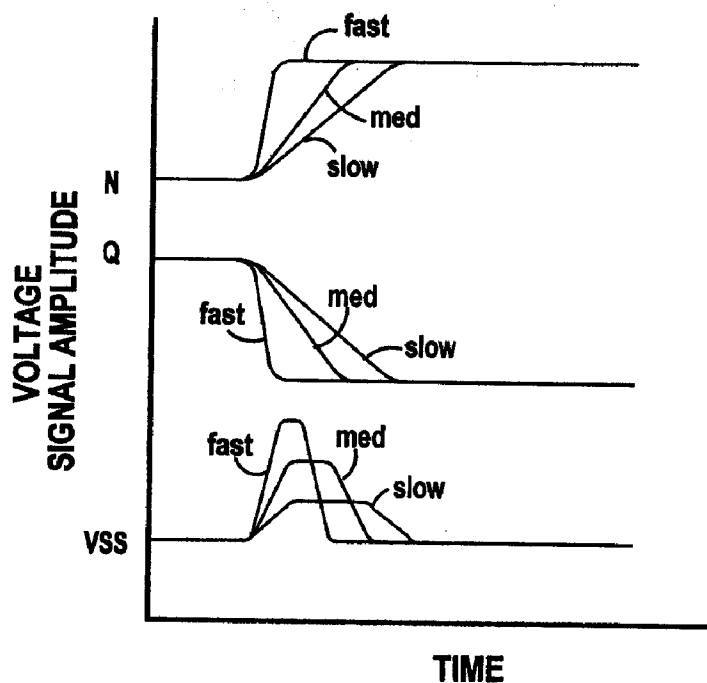


FIG. 7A

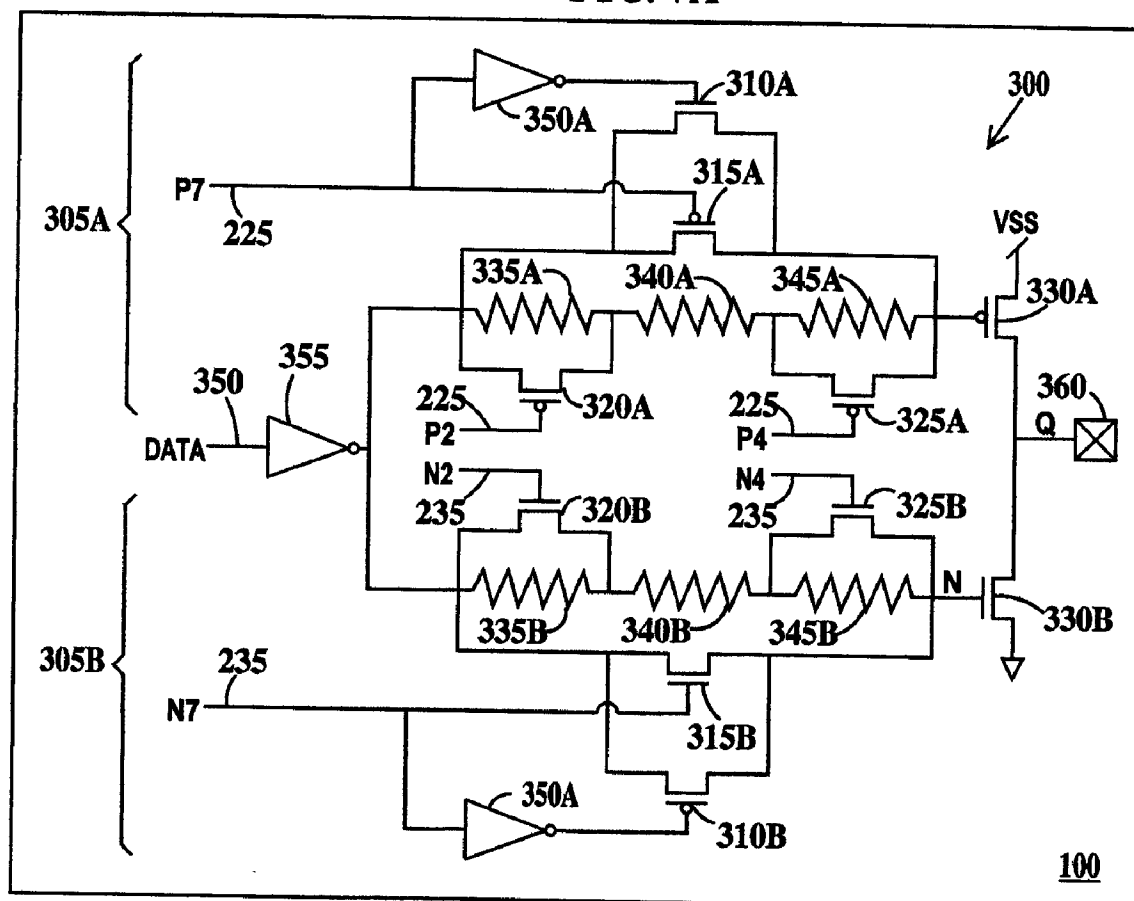


FIG. 7B

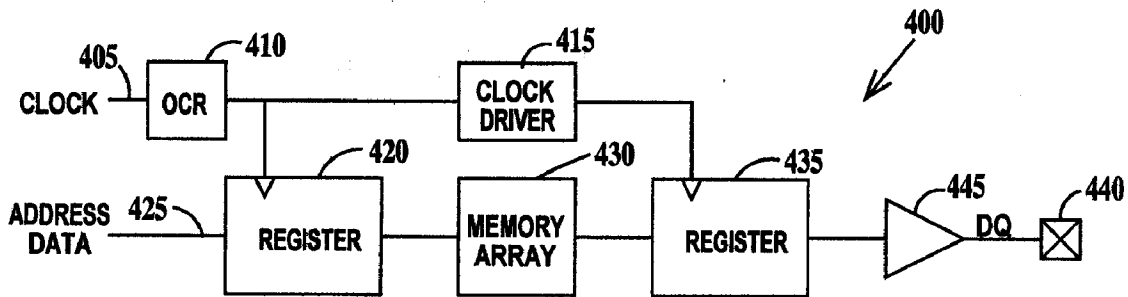


FIG. 8A Related Art

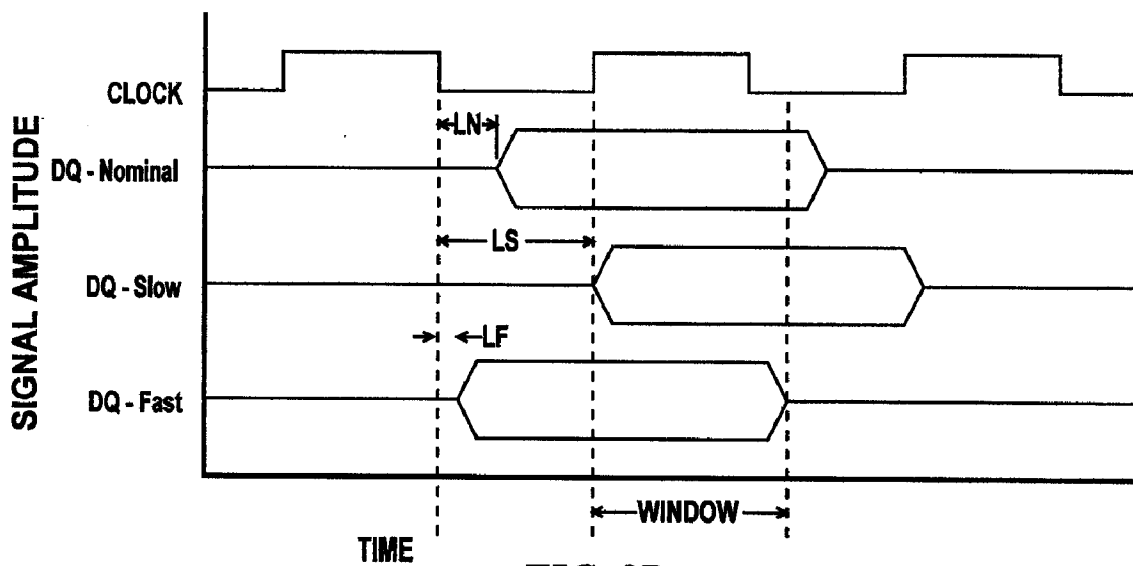


FIG. 8B

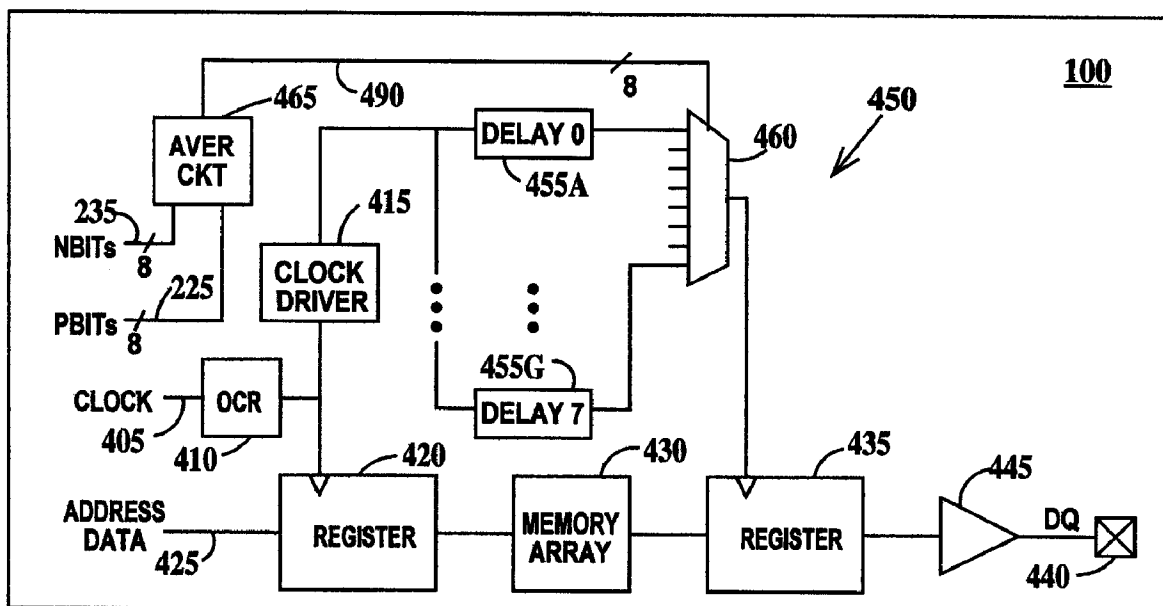


FIG. 8C

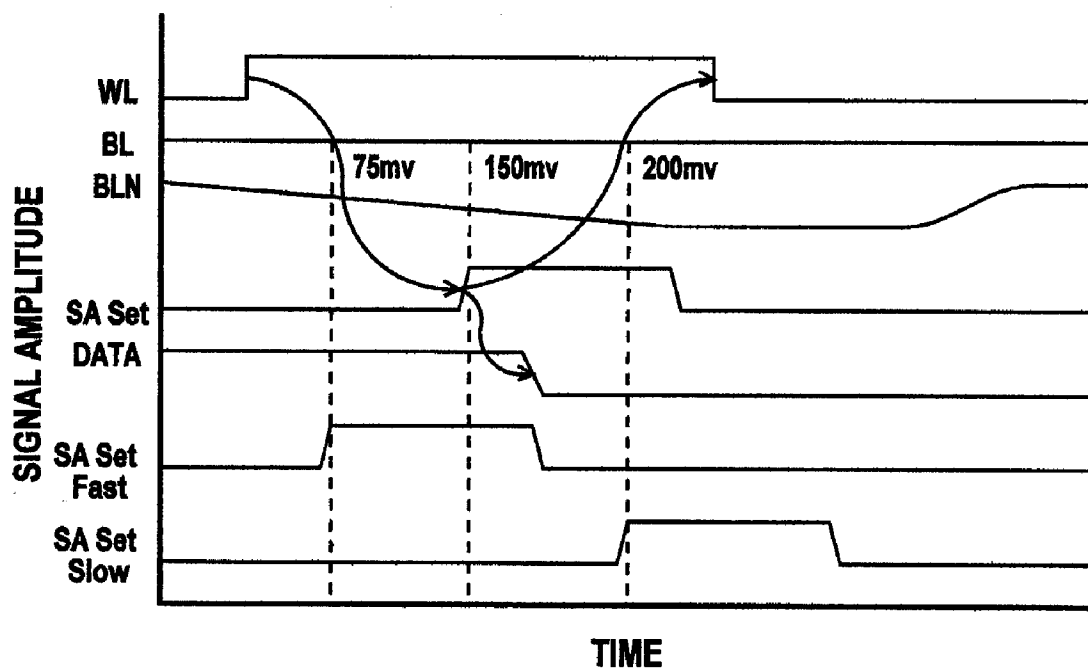


FIG. 9A

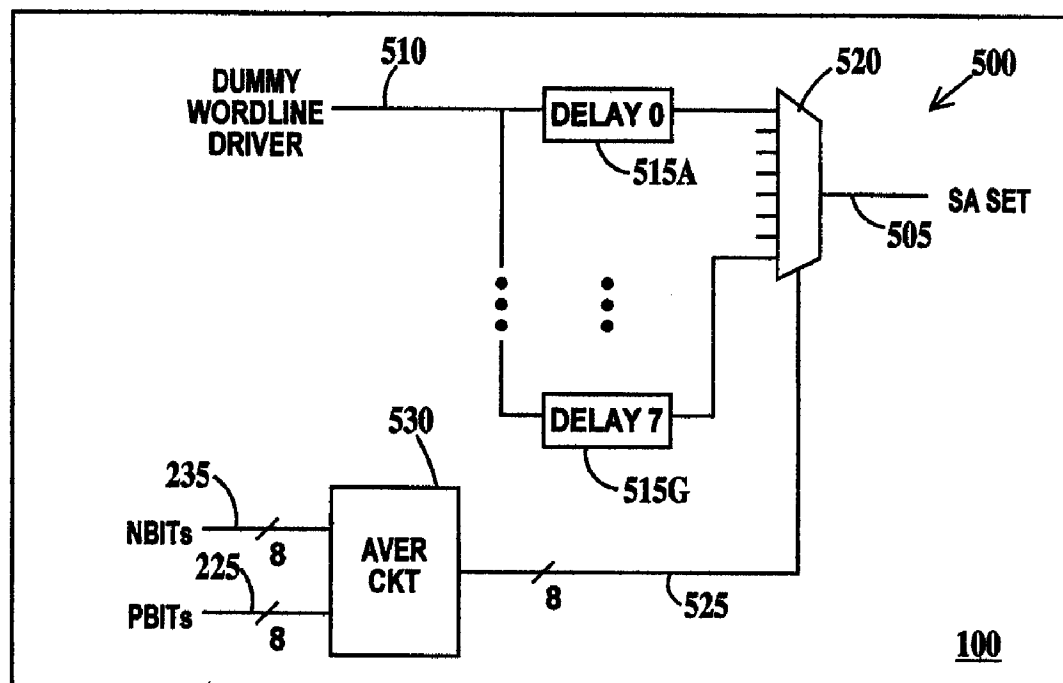


FIG. 9B